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Verilog HDL: Synchronous State Machine - intel.com

This page contains Verilog tutorial, Verilog Syntax, Verilog Quick Reference, PLI, modelling memory and FSM, Writing Testbenches in Verilog, Lot of Verilog Examples and Verilog in One Day Tutorial.

Welcome To Verilog Page - asic-world.com

6 ECE 232 Verilog tutorial 11 Specifying Boolean Expressions ° assign keyword used to indicate expression ° Assignment takes place continuously ° Note new symbols specific for Verilog ° OR -> | ° AND -> & ° NOT -> ~ //HDL Example 3 //----//Circuit specified with Boolean equations

Basic Verilog - UMass Amherst

Now let us try to understand the code. /* This is multi line comment */ and // this is single line comment, Comments are same as in C language.. In verilog, one circuit is represented by set of "modules".

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